

# Transistor computer

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A **transistor computer** is a computer which uses discrete transistors instead of vacuum tubes. The "first generation" of electronic computers used vacuum tubes, which generated large amounts of heat, were bulky, and were unreliable. A "second generation" of computers, through the late 1950s and 1960s featured boards filled with individual transistors and magnetic memory cores. These machines remained the mainstream design into the late 1960s, when integrated circuits started appearing and led to the "third generation" machines.

## The first transistor computer

The University of Manchester's experimental Transistor Computer was first operational in November 1953 and it is widely believed to be the first transistor computer to come into operation anywhere in the world. There were two versions of the Transistor Computer, the prototype, operational in 1953, and the full-size version, commissioned in April 1955. The 1953 machine had 92 point-contact transistors and 550 diodes, manufactured by STC. It had a 48-bit machine word.<sup>[1]</sup> The 1955 machine had a total of 200 point-contact transistors and 1300 point diodes,<sup>[1]</sup> which resulted in a power consumption of 150 watts. There were considerable reliability problems with the early batches of transistors and the average error free run in 1955 was only 1.5 hours. The Computer also used a small number of tubes in its clock generator, so it was not the first *fully* transistorized machine.<sup>[2]</sup>

The design of a full-size Transistor Computer was subsequently adopted by the Manchester firm of Metropolitan-Vickers, who changed all the circuits to more reliable types of junction transistors.<sup>[1]</sup> The production version was known as the Metrovick 950 and was built from 1956 to the extent of six<sup>[1]</sup> or seven machines,<sup>[3]</sup> which were "used commercially within the company"<sup>[3]</sup> or "mainly for internal use".<sup>[1]</sup>

## Other early machines

During the mid-1950s a series of similar machines appeared. These included the Bell Laboratories TRADIC, completed in January 1954 also incorporated a single high-power output vacuum-tube amplifier to supply its 1-MHz clock power.<sup>[4]</sup>

The first *fully* transistorized computer was either the Harwell CADET which first operated in February 1955, although the price paid for this was that it only operated at the slow speed of 58 kHz,<sup>[citation needed]</sup> or the prototype IBM 604 transistor calculator, described in the next section. The Burroughs Corporation claimed the SM-65 Atlas ICBM / THOR ABLE guidance computer (MOD 1) that it delivered to the US Air Force at the Cape Canaveral missile range in June 1957 was "the world's first operational transistorized computer".<sup>[citation needed]</sup>

In Japan the ETL Mark III began operation in July 1956; the Canadian DRTE Computer in 1957, while in Austria, the Mailüfterl was completed in May 1958,<sup>[5]</sup> being the first transistorized computers in Asia and mainland Europe.

## The first commercial fully transistorized calculator

In April 1955,<sup>[6]</sup> IBM announced the IBM 608 transistor calculator which was first shipped in December 1957.<sup>[7]</sup> IBM and several historians thus consider the IBM 608 the first all solid-state computing machine commercially marketed.<sup>[6][8][9][10]</sup> The development of the 608 was preceded by the prototyping of an experimental all-transistor version of the 604. This was built and demonstrated in October 1954, but was not commercialized.<sup>[7][9][11]</sup>

## The first commercial fully transistorized large-scale computer

The Philco Transac models S-1000 scientific computer and S-2000 electronic data processing computer, were the first commercially produced large-scale all transistor computers, which were introduced in 1957. The Philco computer name "Transac" stands for Transistor-Automatic-Computer. Both of these Philco computer models, used the surface-barrier transistor in its circuitry designs, which was the world's first high-frequency transistor that was suitable for high-speed computers.<sup>[12][13][14]</sup> The surface-barrier transistor was developed by Philco in 1953.<sup>[15]</sup>

In Italy, Olivetti's first commercial fully transistorized computer was the Olivetti Elea 9003, being sold from 1959.<sup>[16]</sup>

## Schools and hobbyists

First generation computers were largely out of reach of schools and hobbyists who wished to build their own, largely because of the cost of the large number of vacuum tubes required (though relay-based computer projects *were* undertaken<sup>[17]</sup>). The fourth generation (VLSI) was also largely out of reach, too, due to most of the design work being inside the integrated circuit package (though this barrier, too, was later removed<sup>[18]</sup>). So, second and third generation computer design (transistors and SSI) were perhaps the best suited to being undertaken by schools and hobbyists.<sup>[19]</sup>

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## External links

- The first transistor computer (<http://www.computer50.org/kgill/transistor/trans.html>) Virtual Museum of Manchester Computing

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